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contribute to the bonding itself but has the effect of reducing the coefficient of linear expansion, is uniformly insulating resin 306m, then the distributed in the inorganic filler 6f comes in contact with the surface of the board 4 or the IC chip 1. This leads to a reduction in the amount of adhesive contributing to the bonding and to degraded adhesion. If the separation between the IC chip 1 or the board 4 and the adhesive occurs, moisture enters the portion, causing the corrosion of the electrode of IC chip If the separation progresses from the 1 or the like. separated portion, then the very bonding of the IC chip 1 to the board 4 becomes defective, causing a defective electrical connection.

In contrast to this, according to the twentyninth embodiment and the various modification examples
thereof as described above, the adhesive strength can be
improved with the effect of reducing the coefficient of
linear expansion by the inorganic filler 6f kept provided.
This improves the adhesion strength to the IC chip 1 and
the board 4 and improves the reliability.

Furthermore, when the portion 700 or the resin layer 6x that has a small amount of inorganic filler 6f is arranged on the IC chip side or when the inorganic filler distribution is reduced on the IC chip side, the portion 700 or the resin layer 6x is able to have an improved

adhesive strength to the passivation film made of silicon nitride or silicon oxide on the IC chip surface. It is also possible to properly select and employ an insulating resin that improves adhesion to the film material used on the IC chip surface. Moreover, by reducing the elastic modulus in the vicinity of the IC chip, the stress concentration in the encapsulating sheet material, which is one example of the insulating resin layer, is alleviated. If such a structure is adopted when the material used for the board 4 is as hard as ceramic (with high elastic modulus), then there is advantageously provided matching with the encapsulating sheet material in the vicinity of the board in terms of elastic modulus and the coefficient of linear expansion.

In the case where the portion 700 or the resin layer 6x having a small amount of inorganic filler 6f is arranged on the board side or in the case where the inorganic filler distribution is reduced on the board side, if a bending stress is applied when the board 4 is assembled into the casing of electric equipment when a bending force is applied to the board 4 as in the case of a resin board or a flexible board (FPC), then the portion or layer can be used for the purpose of improving the adhesion strength exerted between the board 4 and the encapsulating sheet that serves as an example of the insulating resin

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In the case where the surface layer on the IC chip side is constructed of a protecting film formed of a polyimide film, the elastic modulus and the coefficient of linear expansion vary in steps from the IC chip 1 to the board 4 when the adhesion of the insulating resin is generally satisfactory and has no problem, allowing the encapsulating sheet to be made of a hard material on the IC chip side and of a soft material on the board side. the stress generation inside the arrangement, reduced, and therefore, the encapsulating sheet is reliability is improved.

Furthermore, in the case where the portion 700 or the resin layers 6x and 6z having a small amount of inorganic filler 6f are arranged on both the IC chip side and the board side or in the case where the inorganic filler distribution is reduced on both the IC chip side and the board side, a compatibility is assured on both the IC chip side case and the board side case. This enables the improvement in adhesion on both the IC chip side and the board side and the connection of both the IC chip 1 and the board 4 with high reliability with a reduced coefficient of linear expansion. Moreover, it is allowed to select and employ an insulating resin of excellent adhesion and resin wettability according to the material of the surface on the IC chip side and the board material. Moreover, the